

FIG. 1

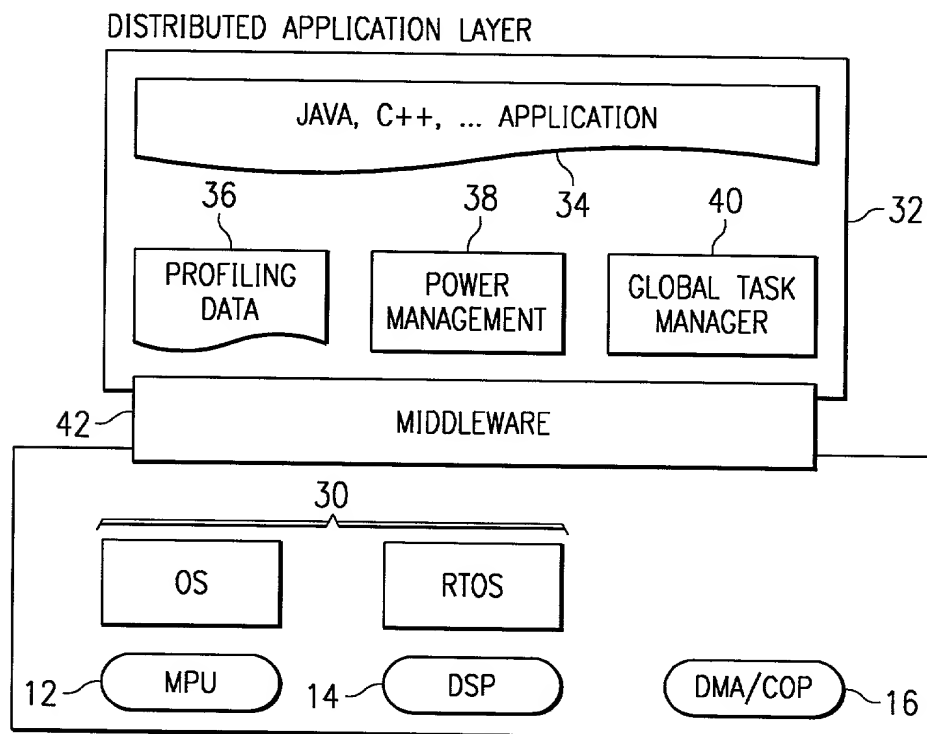


FIG. 2

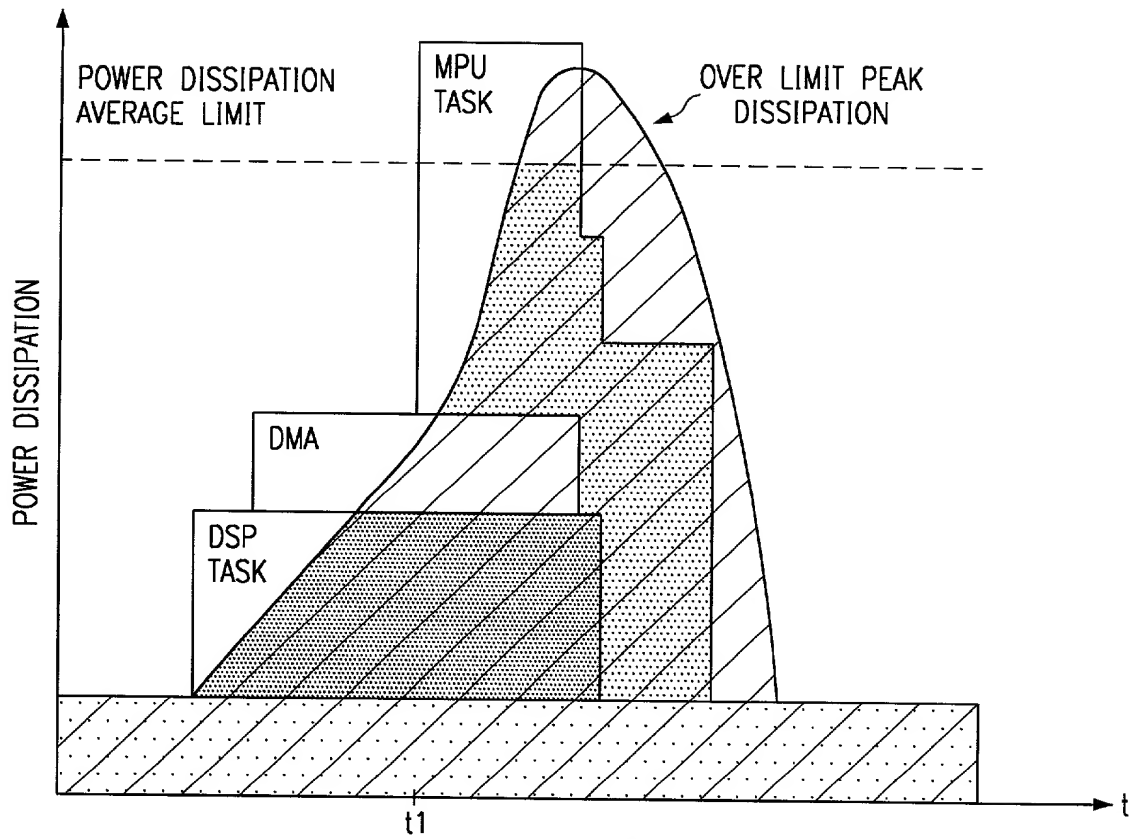


FIG. 3a

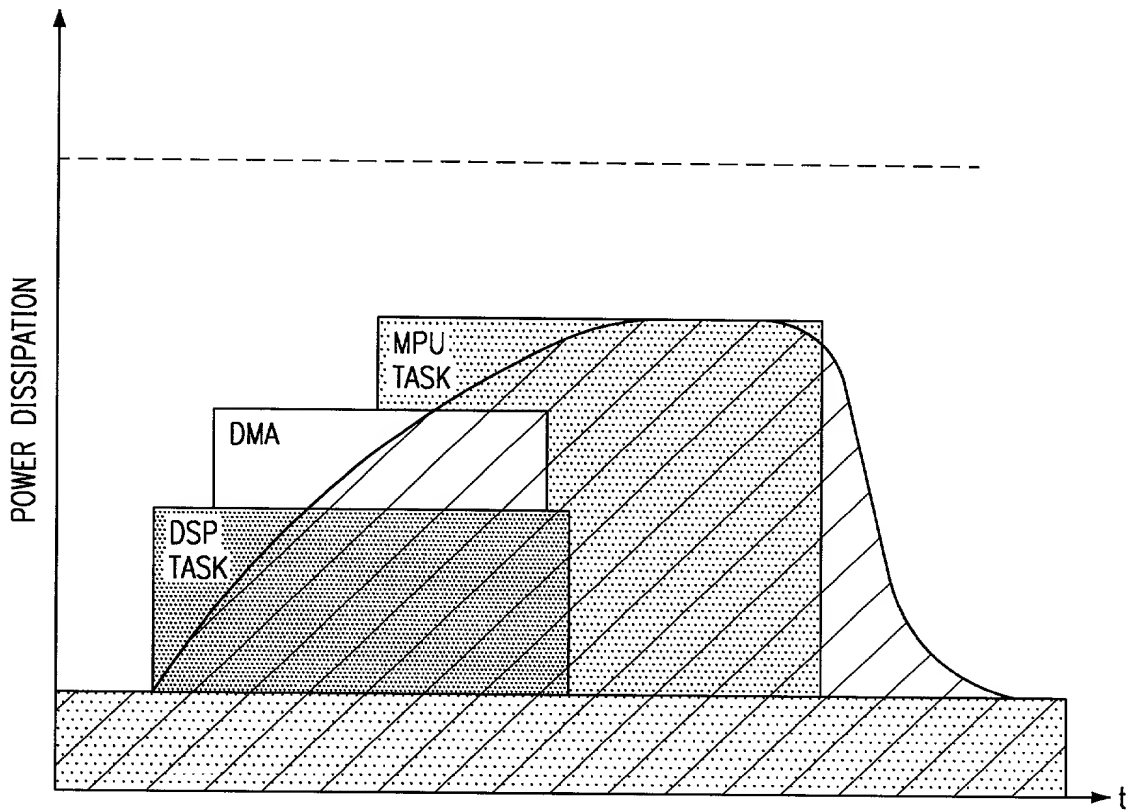


FIG. 3b

FIG. 4a

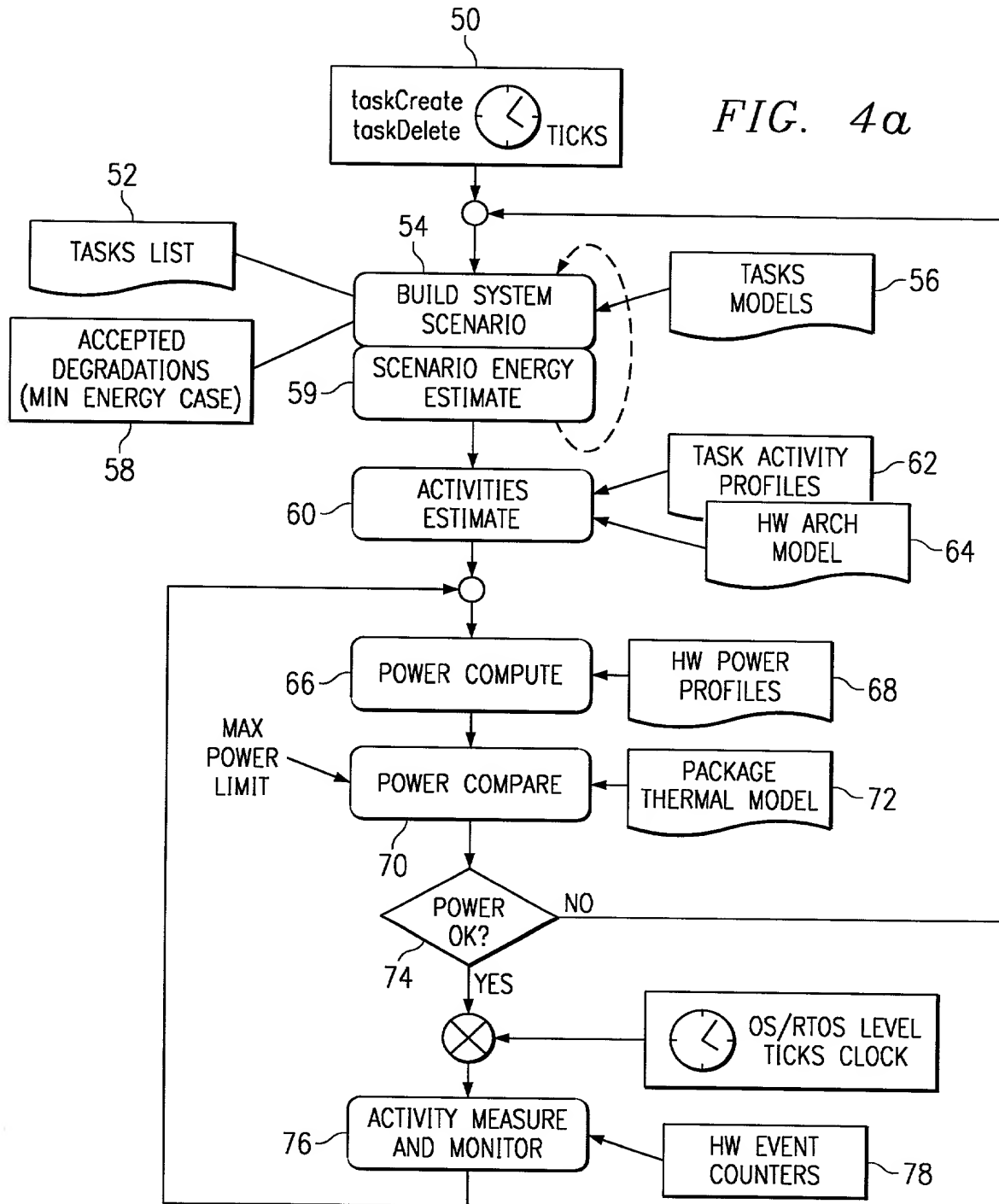
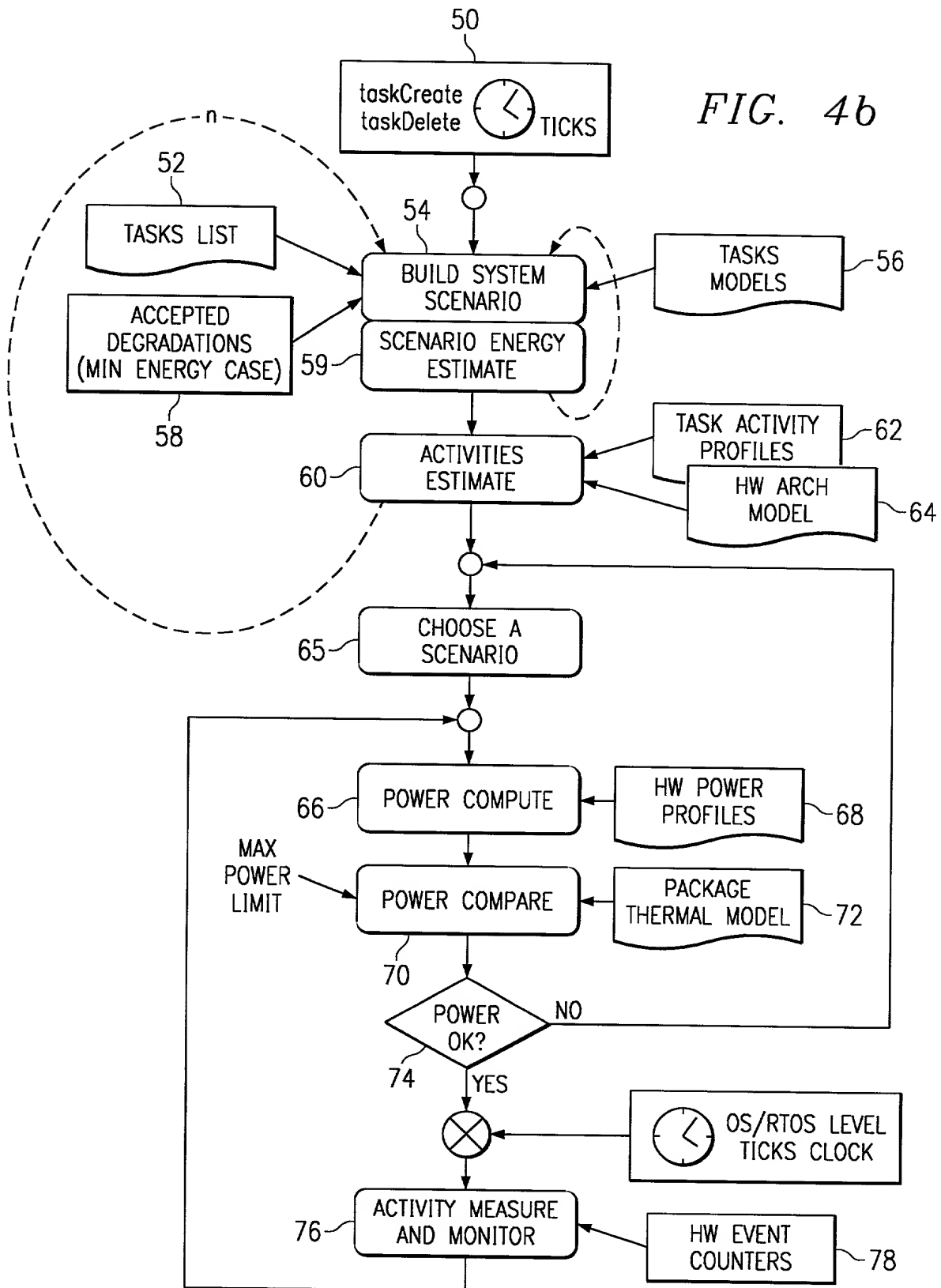


FIG. 4b



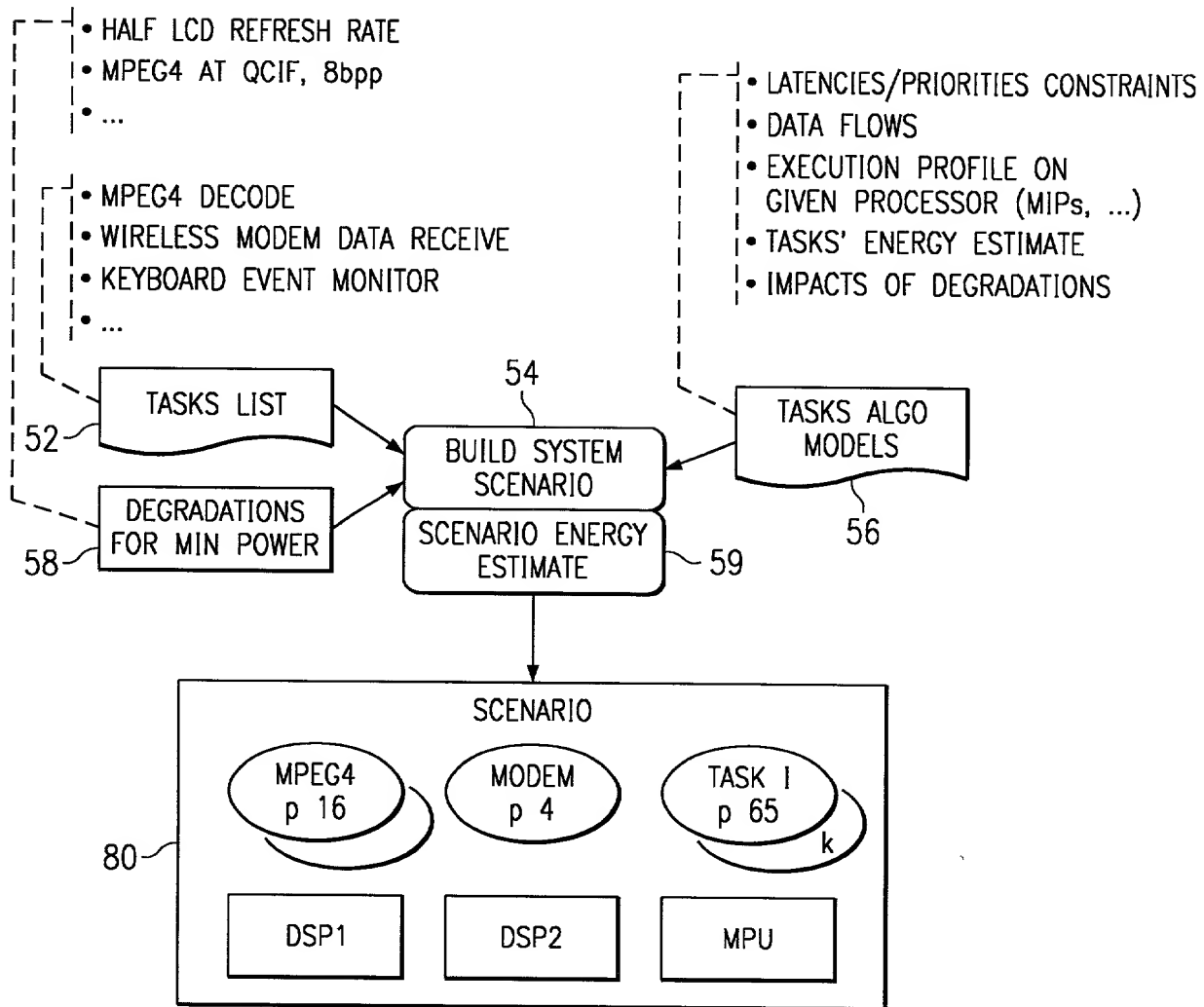


FIG. 5

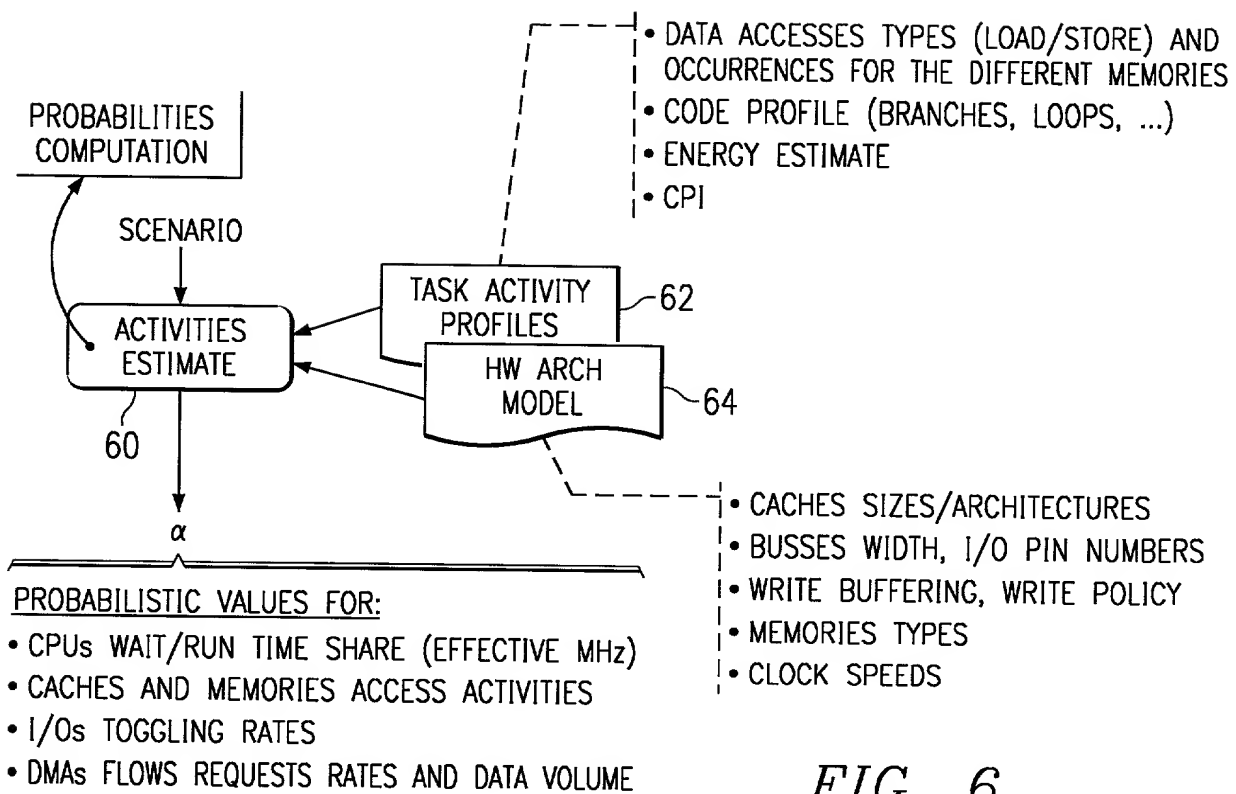


FIG. 6

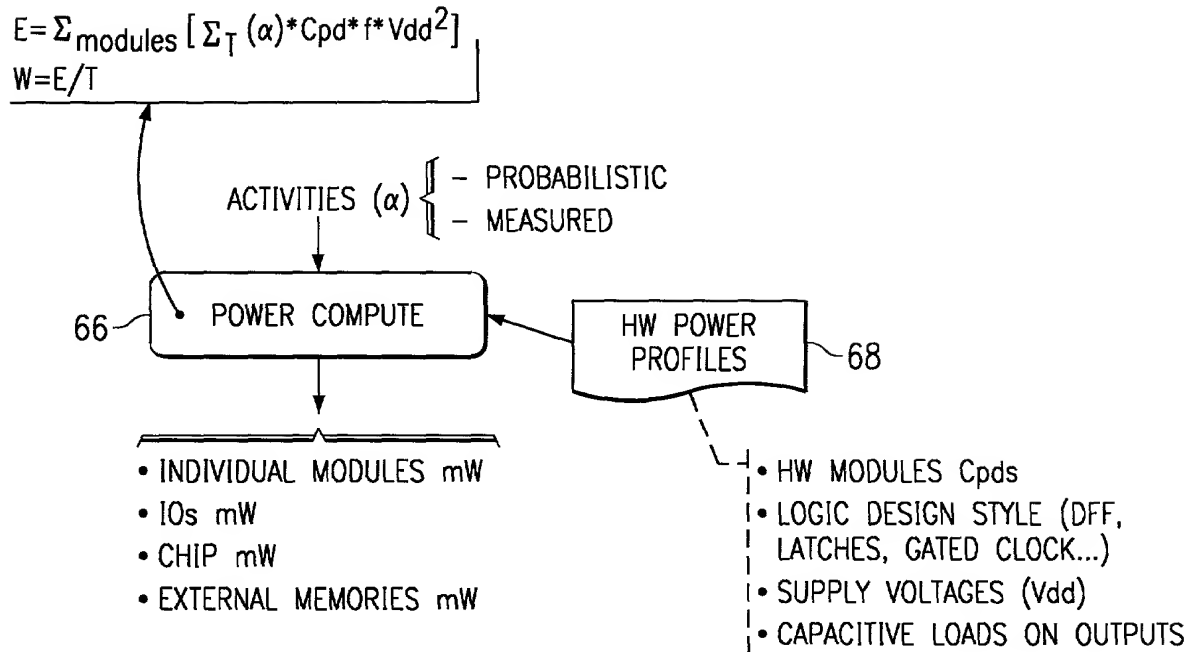


FIG. 7

- ▶ TASK ATTACHED TO A GIVEN PROCESSOR
- ▶ SCHEDULED AT OS/RTOS LEVEL

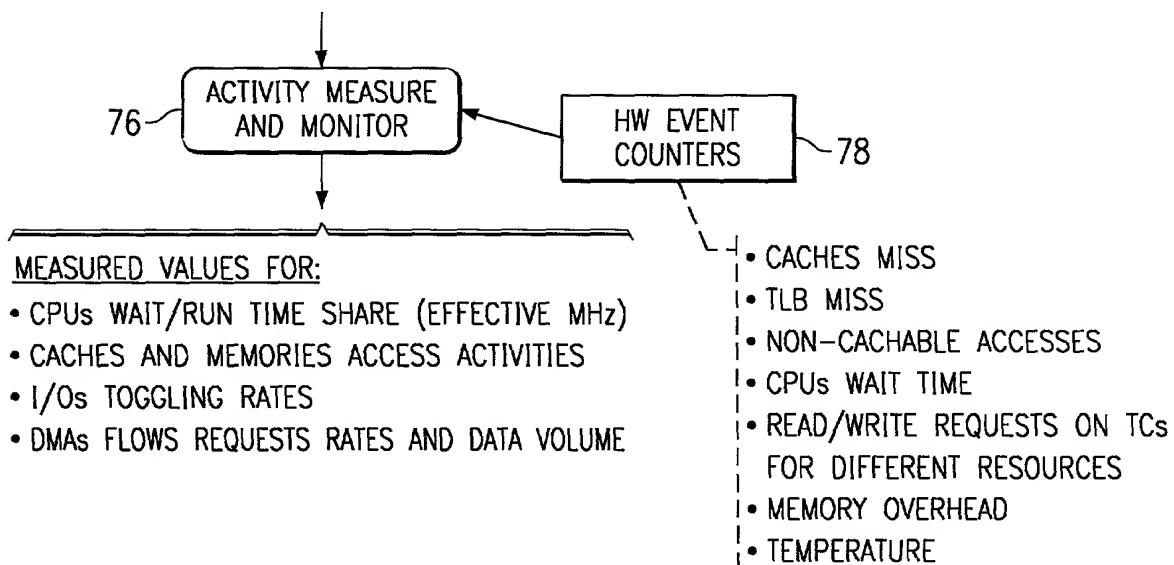
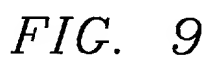
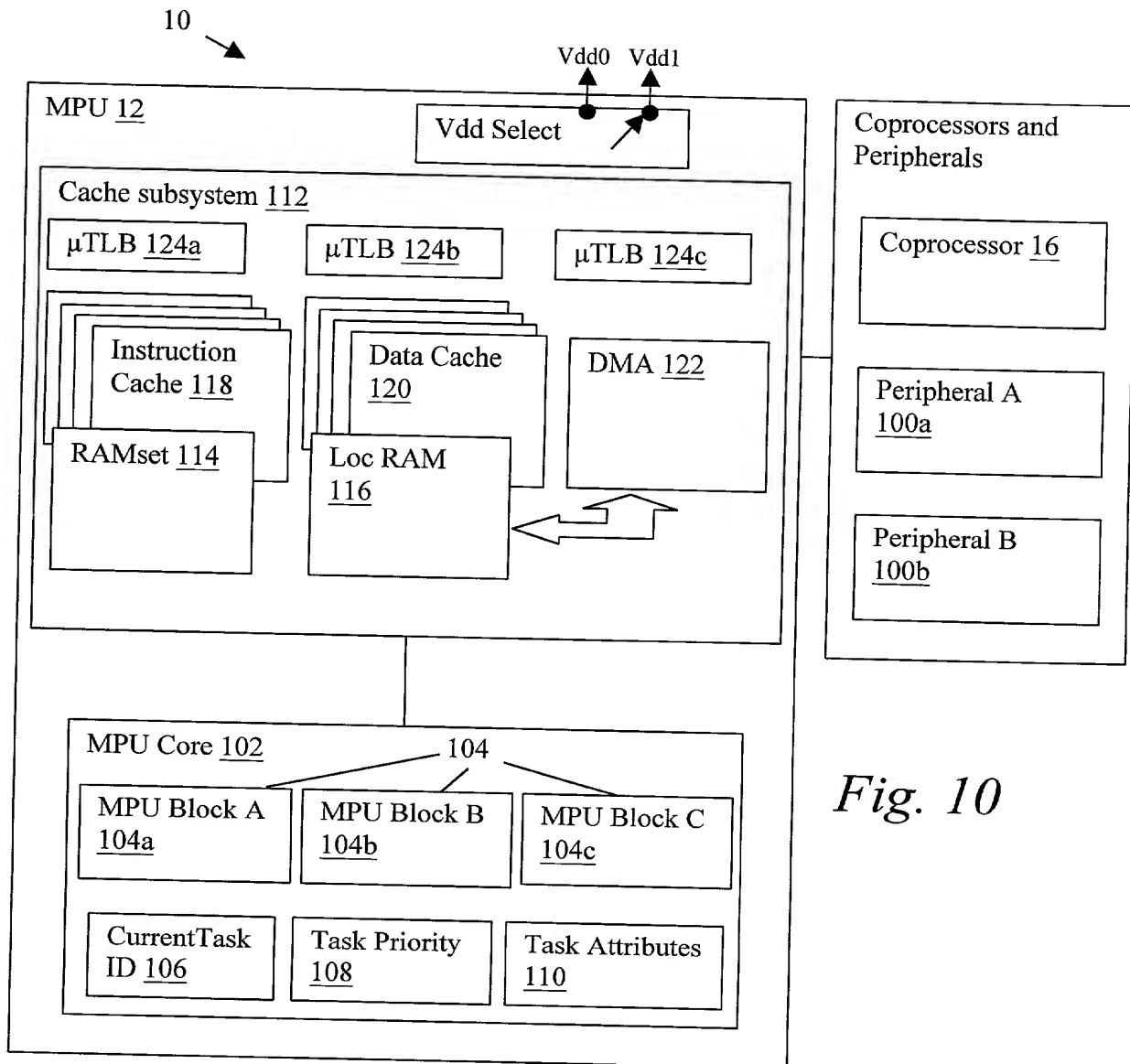


FIG. 8







*Fig. 10*

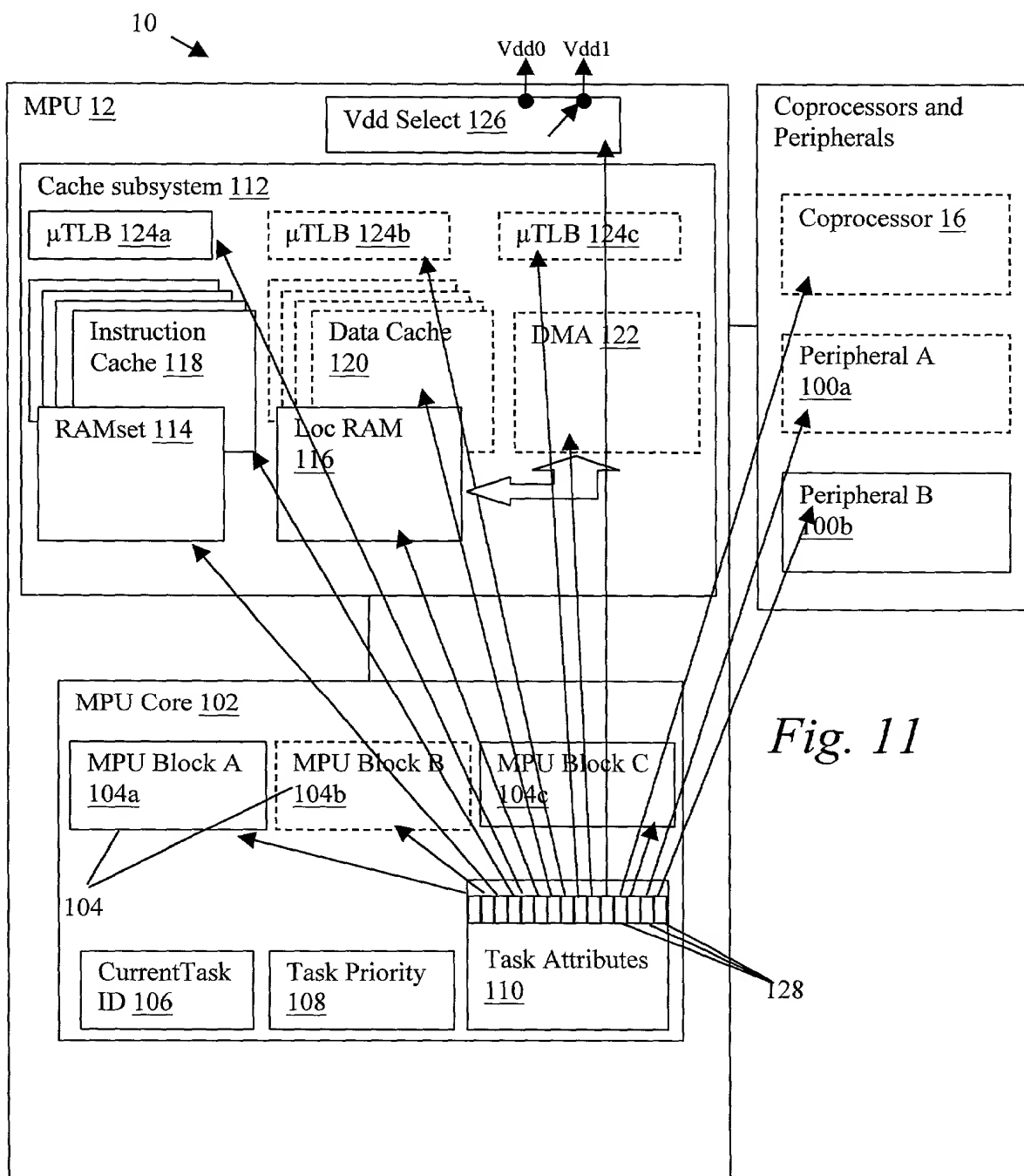
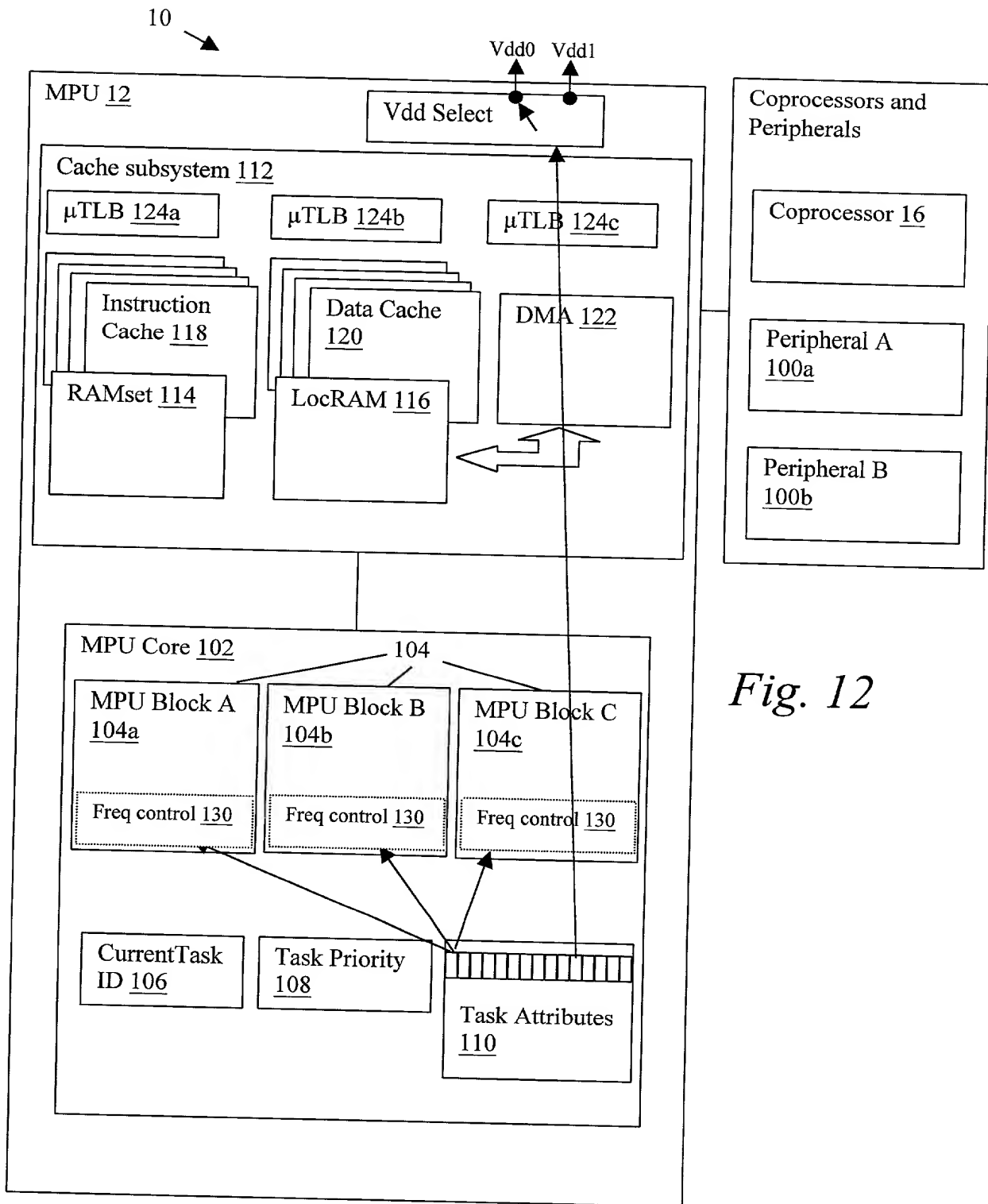
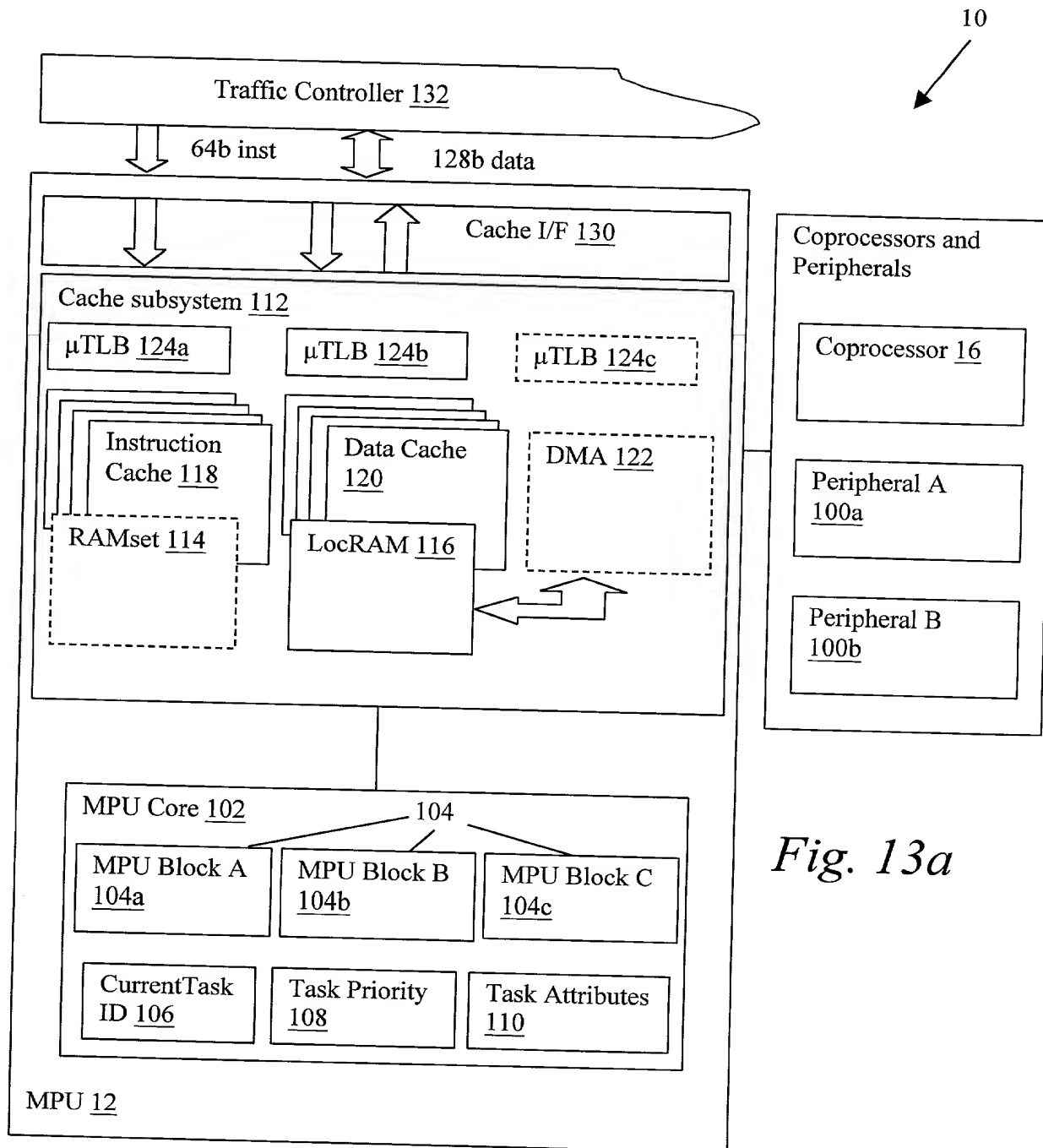
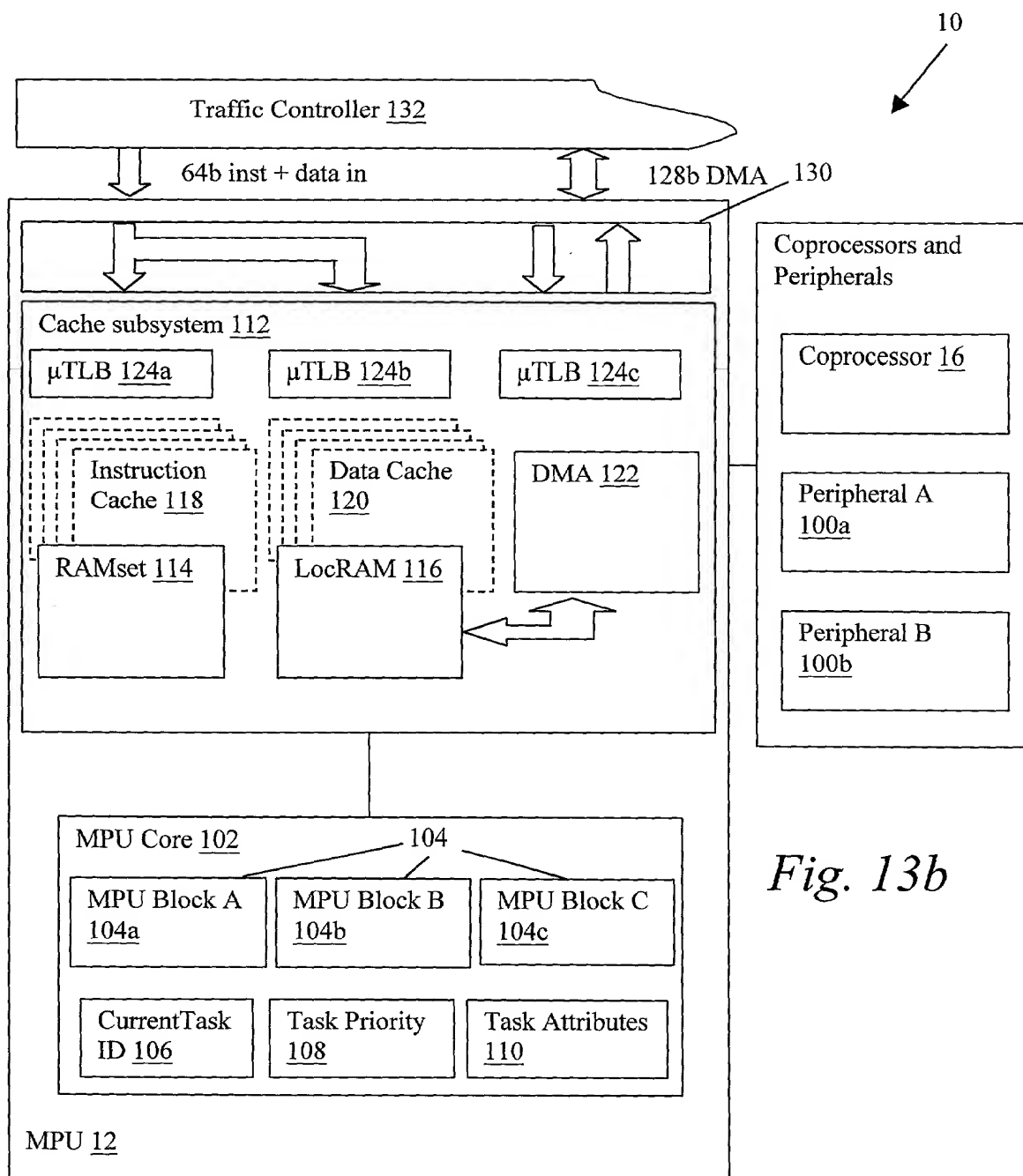


Fig. 11

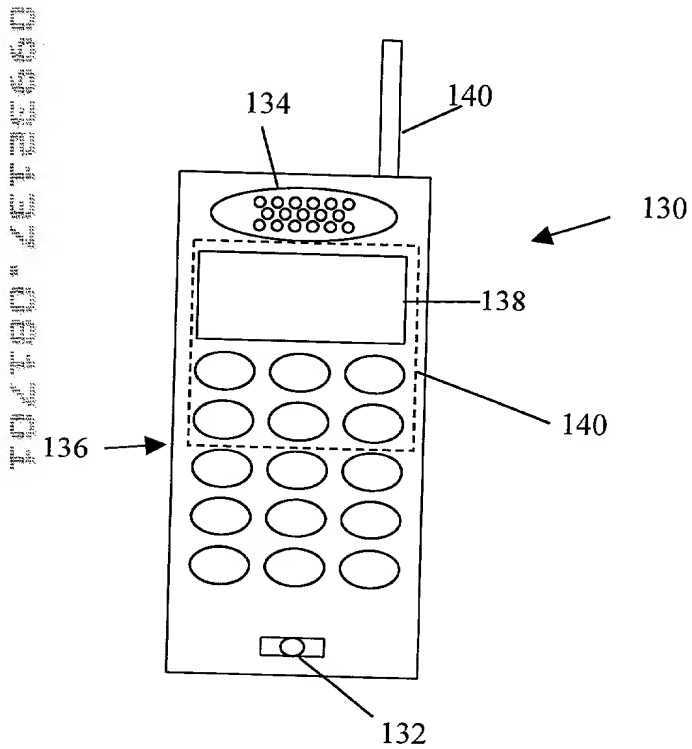
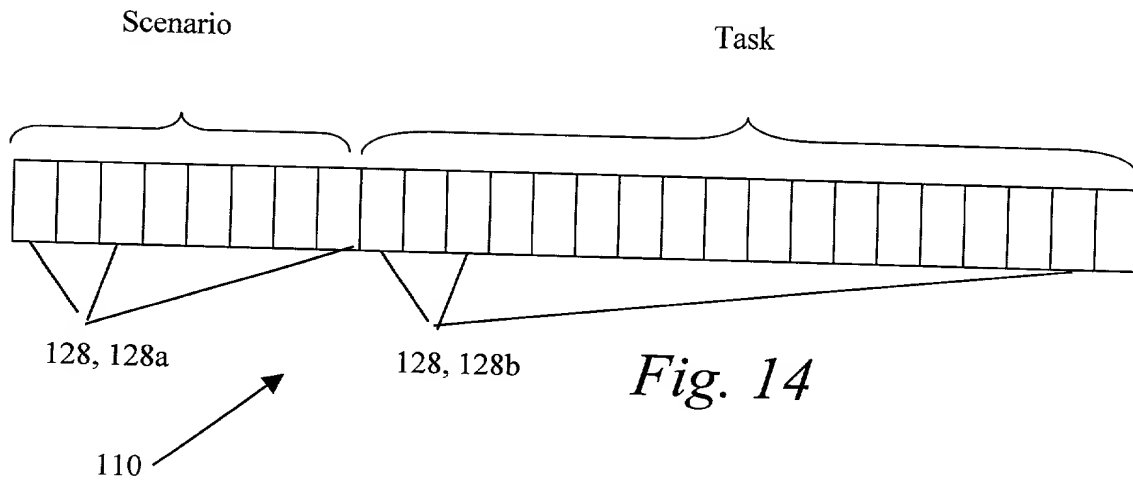


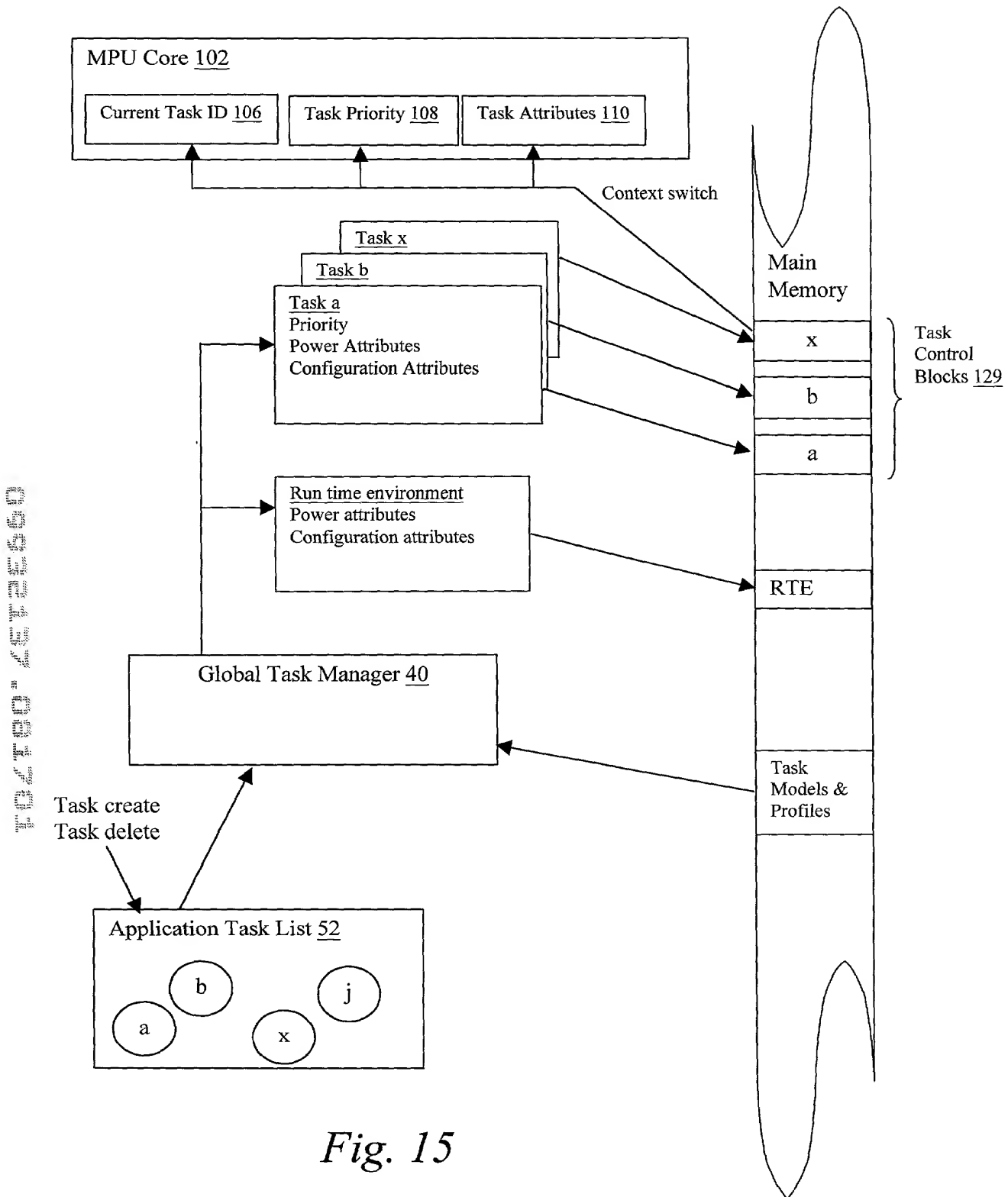
*Fig. 12*





*Fig. 13b*





*Fig. 15*